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Nohbyung Park, Alice Parker

July 1986 **Proceedings of the 23rd ACM/IEEE conference on Design automation DAC '86**

Publisher: IEEE Press

Full text available:  pdf(744.87 KB)

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This paper describes a set of techniques for the synthesis of pipelined data paths, and presents Sehwa, a program which performs such synthesis. The task includes the generation of data paths from a data flow graph along with a clocking scheme which overlaps execution of multiple tasks. Some examples which Sehwa has designed are given. Sehwa can find the minimum cost design, the highest performance design, and other designs between these two in the design space. We believe Sehwa to be the f ...

2 [Sehwa: A program for synthesis of pipelines](#)

N. Park, A. Parker

June 1988 **Papers on Twenty-five years of electronic design automation 25 years of DAC**

Publisher: ACM Press

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Mark Smotherman, Sanjay Krishnamurthy, P. S. Aravind, David Hunnicutt

September 1991 **Proceedings of the 24th annual international symposium on Microarchitecture MICRO 24**

Publisher: ACM Press

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John C. Gyllenhaal, Wen-mei W. Hwu, B. Ramabrioehna Rau

December 1996 **Proceedings of the 29th annual ACM/IEEE international symposium on Microarchitecture MICRO 29**

Publisher: IEEE Computer Society

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